

9DATA SHEET

OLH5730/5731: Hermetic Low Input Current, Dual-Channel Optocouplers

Features

- Rugged and reliable hermetic Dual Inline Package (DIP)
- Performance guaranteed over full military temperature range
- High isolation voltage, 3000 VDC
- · Low input current, 0.5 mA
- Low power consumption
- High Common Mode Rejection (CMR)
- · Radiation tolerant design
- · High-density, dual-channel package

Description

The OLH5730/5731 are dual-channel, hermetic 8-pin DIP optocouplers for low input current applications. The OLH5731 product is a 100 percent high-reliability screened version of the OLH5730.

Each channel consists of an Aluminum Gallium Arsenide (AlGaAs) LED optically coupled to an integrated photo-diode, split-Darlington detector. The AlGaAs LED provides superior low current performance. The split-Darlington open collector output results in high gain and low saturation voltage.

The OLH5730/5731 products are functionally compatible to the HCPL2730/2731 and 6N140A optocouplers. The performance of the OLH5730/5731 products under a radiation environment is significantly improved over standard photo-transistors.

Special low input current or Current Transfer Ratio (CTR) selection are available upon request.

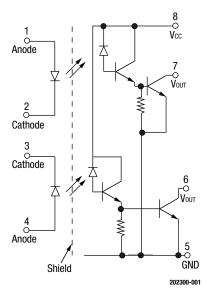


Figure 1. OLH5730/5731 Block Diagram

Figure 1 shows the OLH5730/5731 functional block diagram. Table 1 provides the OLH5730/5731 absolute maximum ratings. Table 2 provides the OLH5730/5731 electrical specifications.

Figures 2 through 5 illustrate the OLH5730/5731 typical performance characteristics. Figure 6 shows the OLH5730/5731 switching test circuit. Figure 7 provides the OLH5730/5731 package dimensions.

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Table 1. OLH5730/5731 Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Coupled				
Input to output isolation voltage ¹	VDC	-3000	+3000	V
Storage temperature range	Тѕтс	-65	+150	°C
Operating temperature range	Та	-55	+125	°C
Lead solder temperature (1.6 mm below the seating plane)			+260 for 10 sec	°C
Input Diode				
Average input current ²	loo		10	mA
Peak forward current (≤1 ms duration)	l _F		20	mA
Reverse voltage	VR		5	V
Output Detector				
Average output current			+40	mA
Supply voltage	Vcc	-0.5	+18.0	V
Output voltage	Vоит	-0.5	+18.0	V
Power dissipation ³	Po		+50	mW

¹ Measured between pins 1, 2, 3, and 4 shorted together, and pins 5, 6, 7, and 8 shorted together.

CAUTION: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

² Derate I_F at 0.33 mA/°C above 110 °C.

 $^{^3}$ Output power is the collector output power plus the total supply power. Derate at 1.66 mW/°C above 110 °C.

Table 2. 0LH5730/5731 Electrical Specifications 1 ($T_A = -55$ °C to +125 °C, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Minimum	Typical	Maximum	Units
Current transfer ratio ²	CTR	$I_F = 0.5 \text{ mA}, V_0 = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	300			%
		$I_F = 1.6 \text{ mA}, V_0 = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	300			%
		$I_F = 5.0 \text{ mA}, V_0 = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	200			%
Logic low output voltage	Vol	$I_F = 0.5 \text{ mA}, I_{OL} = 1.5 \text{ mA}, V_{CC} = 4.5 \text{ V}$		0.1	0.4	V
		$I_F = 1.6 \text{ mA}, I_{OL} = 4.8 \text{ mA}, V_{CC} = 4.5 \text{ V}$		0.1	0.4	V
		$I_F = 5.0 \text{ mA}, I_{OL} = 10 \text{ mA}, V_{CC} = 4.5 \text{ V}$		0.2	0.4	٧
Logic high output current	Іон/ Іох	IF = 2 μ A, IF = 10 mA (other channel), Vo = Vcc = 18 V		0.005	250.0	μА
Logic low supply current	ICCL	IF = 1.6 mA, Vcc = 18 V		1.0	2.0	mA
Logic high supply current	Іссн	IF = 0 mA, Vcc = 18 V		0.01	40.0	μΑ
Input forward voltage	VF	IF = 1.6 mA	1.0	1.65	2.0	V
Input reverse breakdown voltage	Bvr	IR = 10 μA	3			V
Input to output leakage current ³	lı_o	$R_H \le 50\%$, $T_A = 25$ °C, $V_{I_0} = 3000$ VDC, $t = 1$ s			1	μΑ
Propagation Delay Time:						
Logic high to low	t PHL	IF = 0.5 mA, RL = 4.7 k Ω , Vcc = 5.0 V, TA = 25 °C		26	100	μs
		IF = 1.6 mA, RL = 2.2 k Ω , Vcc = 5.0 V, TA = 25 °C		5	30	μs
		IF = 5.0 mA, RL = 680.0 Ω , Vcc = 5.0 V, TA = 25 °C		2	10	μѕ
Logic low to high	tрLH	IF = 0.5 mA, RL = 4.7 k Ω , Vcc = 5.0 V, TA = 25 °C		28	60	μѕ
		IF = 1.6 mA, RL = 2.2 k Ω , Vcc = 5.0 V, TA = 25 °C		15	50	μѕ
		IF = 5.0 mA, RL = 680.0 Ω , Vcc = 5.0 V, TA = 25 °C		10	30	μs
Common mode transient immunity:						
Logic high level	СМн	$\begin{split} I_F = 0 \text{ mA, Vcc} = 5.0 \text{ V, Ta} = 25 \text{ °C,} \\ R_L = 1.5 \text{ k}\Omega, V_{CM} = 300.0 \text{ Vp-p} \end{split}$	5	≥10		KV/μs
Logic low level	CML	IF = 1.6 mA, Vcc = 5.0 V, TA = 25 °C, RL = 1.5 kΩ, VcM = 300.0 VP-P	5	≥10		KV/μs

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to the device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

 $^{^2}$ CTR is defined as the ratio of the output collector current lc to the forward LED current lF, multiplied by 100%.

 $^{^{3}}$ Measured between pins 1, 2, 3, and 4 shorted together, and pins 5, 6, 7, and 8 shorted together.

Typical Performance Characteristics

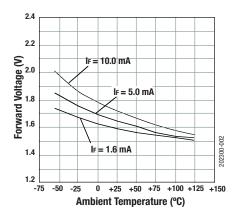


Figure 2. Forward Voltage vs Temperature

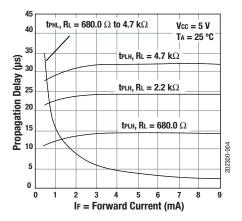


Figure 4. Propagation Delay vs Input Diode Forward Current

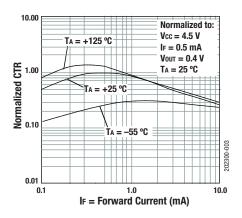


Figure 3. Normalized CTR vs Input Diode Forward Current

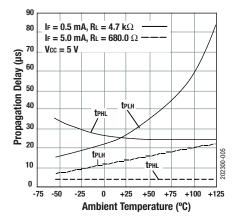


Figure 5. Propagation Delay vs Temperature

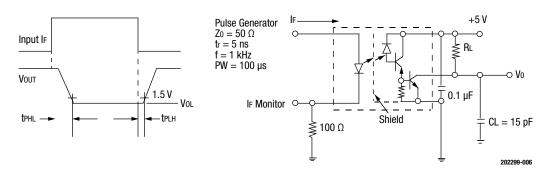


Figure 6. OLH5730/5731 Switching Test Circuit

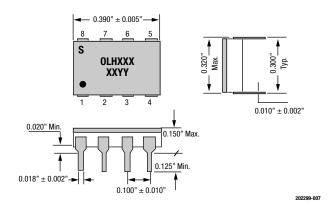


Figure 7. OLH5730/5731 Package Dimensions

Ordering Information

Model Name	Manufacturing Part Number		
OLH5730/5731: Hermetic Low Input Current, Dual-Channel Optocouplers	OLH5730/5731		

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